January 15, 2012

LM78LXX Series

3-Terminal Positive Regulators

General Description

The LM78LXX series of three terminal positive regulators is available with several fixed output voltages making them useful in a wide range of applications. When used as a zener diode/resistor combination replacement, the LM78LXX usually results in an effective output impedance improvement of two orders of magnitude, and lower quiescent current. These regulators can provide local on card regulation, eliminating the distribution problems associated with single point regulation. The voltages available allow the LM78LXX to be used in logic systems, instrumentation, HiFi, and other solid state electronic equipment.

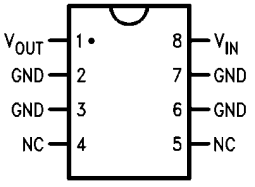
The LM78LXX is available in the plastic TO-92 (Z) package, the plastic SO-8 (M) package and a chip sized package (8-Bump micro SMD) using National's micro SMD package technology. With adequate heat sinking the regulator can deliver 100mA output current. Current limiting is included to limit the peak output current to a safe value. Safe area protection for the output transistors is provided to limit internal power dissipation. If internal power dissipation becomes too high for the heat sinking provided, the thermal shutdown circuit takes over preventing the IC from overheating.

Features

- LM78L05 in micro SMD package
- Output voltage tolerances of ±5% over the temperature range
- Output current of 100mA
- Internal thermal overload protection
- Output transistor safe area protection
- Internal short circuit current limit
- Available in plastic TO-92 and plastic SO-8 low profile packages
- No external components
- Output voltages of 5.0V, 6.2V, 8.2V, 9.0V, 12V, 15V
- See AN-1112 for micro SMD considerations

Connection Diagrams

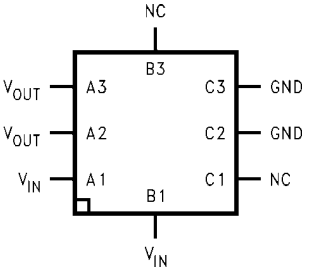
SO-8 Plastic (M)
(Narrow Body)



774402

Top View

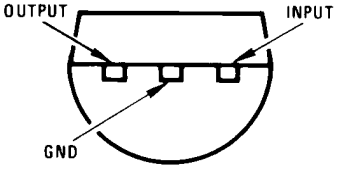
8-Bump micro SMD



774424

Top View
(Bump Side Down)

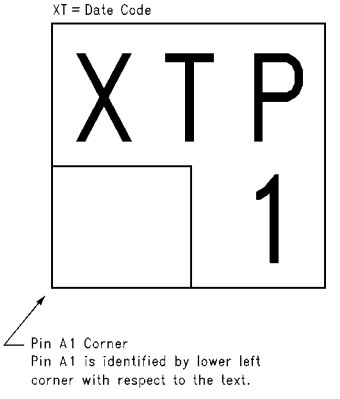
(TO-92)
Plastic Package (Z)



774403

Bottom View

micro SMD Marking Orientation



774433

Top View

Ordering Information

Package Type	NSC Drawing	Output Voltage	Operating Temperature Range	Order Number	Supplied As
micro SMD	BPA08AAB	5.0V	−40°C to 85°C	LM78L05IBP	OBSOLETE
				LM78L05IBPX	OBSOLETE
Thin micro SMD	TPA08AAA	5.0V	−40°C to 85°C	LM78L05ITP	Reel of 250
				LM78L05ITPX	Reel of 3000
		9.0V	−40°C to 85°C	LM78L09ITP	OBSOLETE
				LM78L09ITPX	Reel of 3000
SOIC Narrow	M08A	5.0V	0°C to 125°C	LM78L05ACM	Rail of 95
				LM78L05ACMX	Reel of 2500
			-40°C to 125°C	LM78L05AIM	Rail of 95
				LM78L05AIMX	Reel of 2500
		12V	0°C to 125°C	LM78L12ACM	Rail of 95
				LM78L12ACMX	Reel of 2500
		15V	0°C to 125°C	LM78L15ACM	Rail of 95
				LM78L15ACMX	Reel of 2500
TO-92	Z03A	5.0V	0°C to 125°C	LM78L05ACZ	Box of 1800
		6.2V	0°C to 125°C	LM78L62ACZ	Box of 1800
		8.2V	0°C to 125°C	LM78L82ACZ	OBSOLETE
		9.0V	0°C to 125°C	LM78L09ACZ	OBSOLETE
		12V	0°C to 125°C	LM78L12ACZ	Box of 1800
		15V	0°C to 125°C	LM78L15ACZ	Box of 1800

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/ Distributors for availability and specifications.

Power Dissipation (Note 5)	Internally Limited
Input Voltage	35V
Storage Temperature	−65°C to +150°C
ESD Susceptibility (Note 2)	1kV

Operating Junction Temperature	
LM78LxxACZ, TO-92	0°C to 125°C
LM78LxxACM, SO-8	0°C to 125°C
LM78LxxAIM, SO-8	-40°C to 125°C
LM78LxxIBPX, micro SMD	−40°C to 85°C
LM78LxxITP, Thin micro SMD	−40°C to 85°C
Soldering Information	
Infrared or Convection (20 sec.)	235°C
Wave Soldering (10 sec.)	260°C (lead time)

LM78LXX Electrical Characteristics

Limits in standard typeface are for T_J = 25°C, **Bold typeface applies over the entire operating temperature range of the indicated package.** Limits are guaranteed by production testing or correlation techniques using standard Statistical Quality Control (SQC) methods. Unless otherwise specified: I_O = 40mA, C_I = 0.33µF, C_O = 0.1µF.

LM78L05AC / LM78L05I

Unless otherwise specified, V_{IN} = 10V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		4.8	5	5.2	V
		7V ≤ V_{IN} ≤ 20V 1mA ≤ I_O ≤ 40mA (Note 3)	**4.75**		**5.25**	
		1mA ≤ I_O ≤ 70mA (Note 3)	**4.75**		**5.25**	
ΔV_O	Line Regulation	7V ≤ V_{IN} ≤ 20V		18	75	mV
		8V ≤ V_{IN} ≤ 20V		10	54	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		20	60	
		1mA ≤ I_O ≤ 40mA		5	30	
I_Q	Quiescent Current			3	5	mA
ΔI_Q	Quiescent Current Change	8V ≤ V_{IN} ≤ 20V			**1.0**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage	f = 10 Hz to 100 kHz (Note 4)		40		µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 8V ≤ V_{IN} ≤ 16V	47	62		dB
I_{PK}	Peak Output Current			140		mA
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		−0.65		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			6.7	7	V
θ_{JA}	Thermal Resistance (8-Bump micro SMD)			230.9		°C/W

LM78L62AC

Unless otherwise specified, V_{IN} = 12V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		5.95	6.2	6.45	V
		8.5V ≤ V_{IN} ≤ 20V 1mA ≤ I_O ≤ 40mA (*Note 3*)	**5.9**		**6.5**	
		1mA ≤ I_O ≤ 70mA (*Note 3*)	**5.9**		**6.5**	
ΔV_O	Line Regulation	8.5V ≤ V_{IN} ≤ 20V		65	175	mV
		9V ≤ V_{IN} ≤ 20V		55	125	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		13	80	
		1mA ≤ I_O ≤ 40mA		6	40	
I_Q	Quiescent Current			2	5.5	mA
ΔI_Q	Quiescent Current Change	8V ≤ V_{IN} ≤ 20V			**1.5**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage	f = 10 Hz to 100 kHz (*Note 4*)		50		µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 10V ≤ V_{IN} ≤ 20V	40	46		dB
I_{PK}	Peak Output Current			140		mA
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		−0.75		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			7.9		V

LM78L82AC

Unless otherwise specified, V_{IN} = 14V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		7.87	8.2	8.53	V
		11V ≤ V_{IN} ≤ 23V 1mA ≤ I_O ≤ 40mA (*Note 3*)	**7.8**		**8.6**	
		1mA ≤ I_O ≤ 70mA (*Note 3*)	**7.8**		**8.6**	
ΔV_O	Line Regulation	11V ≤ V_{IN} ≤ 23V		80	175	mV
		12V ≤ V_{IN} ≤ 23V		70	125	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		15	80	
		1mA ≤ I_O ≤ 40mA		8	40	
I_Q	Quiescent Current			2	5.5	mA
ΔI_Q	Quiescent Current Change	12V ≤ V_{IN} ≤ 23V			**1.5**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage	f = 10 Hz to 100 kHz (*Note 4*)		60		µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 12V ≤ V_{IN} ≤ 22V	39	45		dB
I_{PK}	Peak Output Current			140		mA

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		−0.8		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			9.9		V

LM78L09AC / LM78L09I

Unless otherwise specified, V_{IN} = 15V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		8.64	9.0	9.36	V
		11.5V ≤ V_{IN} ≤ 24V 1mA ≤ I_O ≤ 40mA (*Note 3*)	**8.55**		**9.45**	
		1mA ≤ I_O ≤ 70mA (*Note 3*)	**8.55**		**9.45**	
ΔV_O	Line Regulation	11.5V ≤ V_{IN} ≤ 24V		100	200	mV
		13V ≤ V_{IN} ≤ 24V		90	150	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		20	90	
		1mA ≤ I_O ≤ 40mA		10	45	
I_Q	Quiescent Current			2	5.5	mA
ΔI_Q	Quiescent Current Change	11.5V ≤ V_{IN} ≤ 24V			**1.5**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage			70		µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 15V ≤ V_{IN} ≤ 25V	38	44		dB
I_{PK}	Peak Output Current			140		mA
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		−0.9		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			10.7		V

LM78L12AC

Unless otherwise specified, V_{IN} = 19V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		11.5	12	12.5	V
		14.5V ≤ V_{IN} ≤ 27V 1mA ≤ I_O ≤ 40mA (*Note 3*)	**11.4**		**12.6**	
		1mA ≤ I_O ≤ 70mA (*Note 3*)	**11.4**		**12.6**	
ΔV_O	Line Regulation	14.5V ≤ V_{IN} ≤ 27V		30	180	mV
		16V ≤ V_{IN} ≤ 27V		20	110	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		30	100	
		1mA ≤ I_O ≤ 40mA		10	50	
I_Q	Quiescent Current			3	5	mA
ΔI_Q	Quiescent Current Change	16V ≤ V_{IN} ≤ 27V			**1**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage			80		µV

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 15V ≤ V_{IN} ≤ 25	40	54		dB
I_{PK}	Peak Output Current			140		mA
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		–1.0		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			13.7	14.5	V

LM78L15AC

Unless otherwise specified, V_{IN} = 23V

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_O	Output Voltage		14.4	15.0	15.6	V
		17.5V ≤ V_{IN} ≤ 30V 1mA ≤ I_O ≤ 40mA (*Note 3*)	**14.25**		**15.75**	
		1mA ≤ I_O ≤ 70mA (*Note 3*)	**14.25**		**15.75**	
ΔV_O	Line Regulation	17.5V ≤ V_{IN} ≤ 30V		37	250	mV
		20V ≤ V_{IN} ≤ 30V		25	140	
ΔV_O	Load Regulation	1mA ≤ I_O ≤ 100mA		35	150	
		1mA ≤ I_O ≤ 40mA		12	75	
I_Q	Quiescent Current			3	5	mA
ΔI_Q	Quiescent Current Change	20V ≤ V_{IN} ≤ 30V			**1**	
		1mA ≤ I_O ≤ 40mA			**0.1**	
V_n	Output Noise Voltage			90		µV
$\frac{\Delta V_{IN}}{\Delta V_{OUT}}$	Ripple Rejection	f = 120 Hz 18.5V ≤ V_{IN} ≤ 28.5V	37	51		dB
I_{PK}	Peak Output Current			140		mA
$\frac{\Delta V_O}{\Delta T}$	Average Output Voltage Tempco	I_O = 5mA		–1.3		mV/°C
V_{IN} (Min)	Minimum Value of Input Voltage Required to Maintain Line Regulation			16.7	17.5	V

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Electrical specifications do not apply when operating the device outside of its stated operating conditions.

Note 2: Human body model, 1.5 kΩ in series with 100pF.

Note 3: Power dissipation ≤ 0.75W.

Note 4: Recommended minimum load capacitance of 0.01µF to limit high frequency noise.

Note 5: Typical thermal resistance values for the packages are:

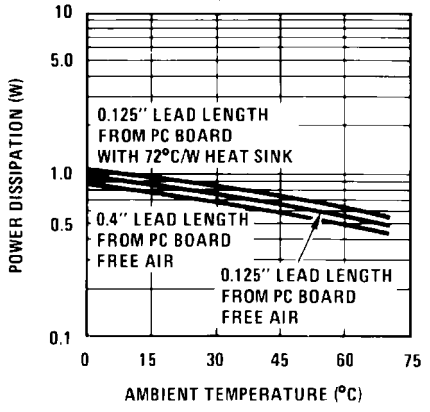
Z Package: θ_{JC} = 60 °C/W, = θ_{JA} = 230 °C/W

M Package: θ_{JA} = 180 °C/W

micro SMD Package: θ_{JA} = 230.9°C/W

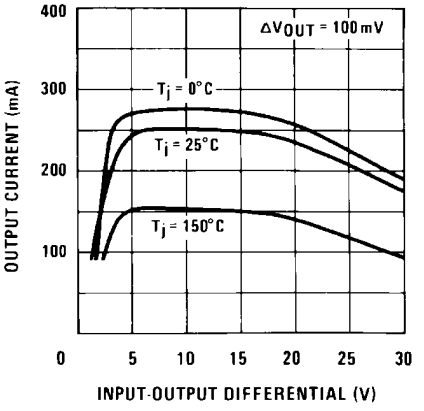
Typical Performance Characteristics

Maximum Average Power Dissipation (Z Package)



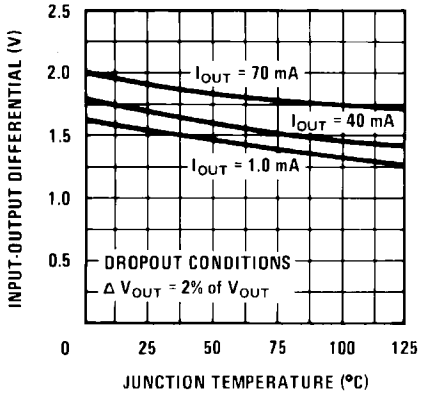
774414

Peak Output Current



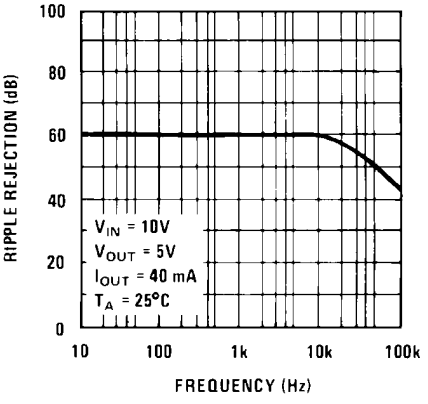
774416

Dropout Voltage



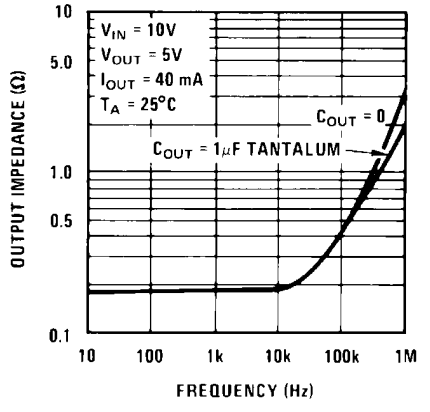
774417

Ripple Rejection



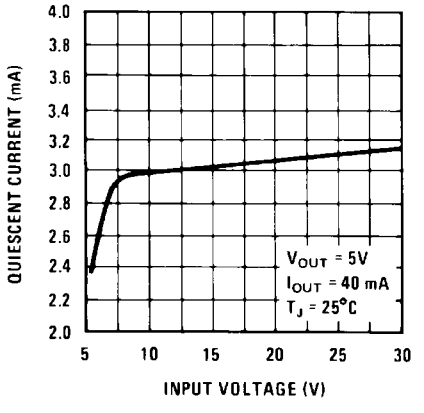
774418

Output Impedance



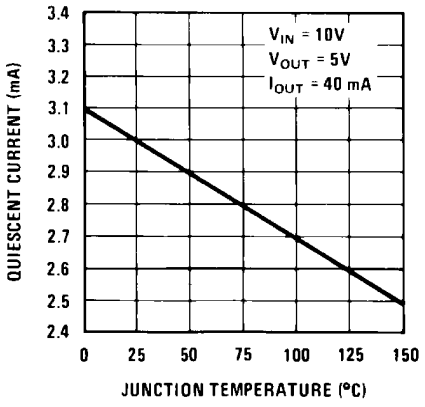
774419

Quiescent Current



774420

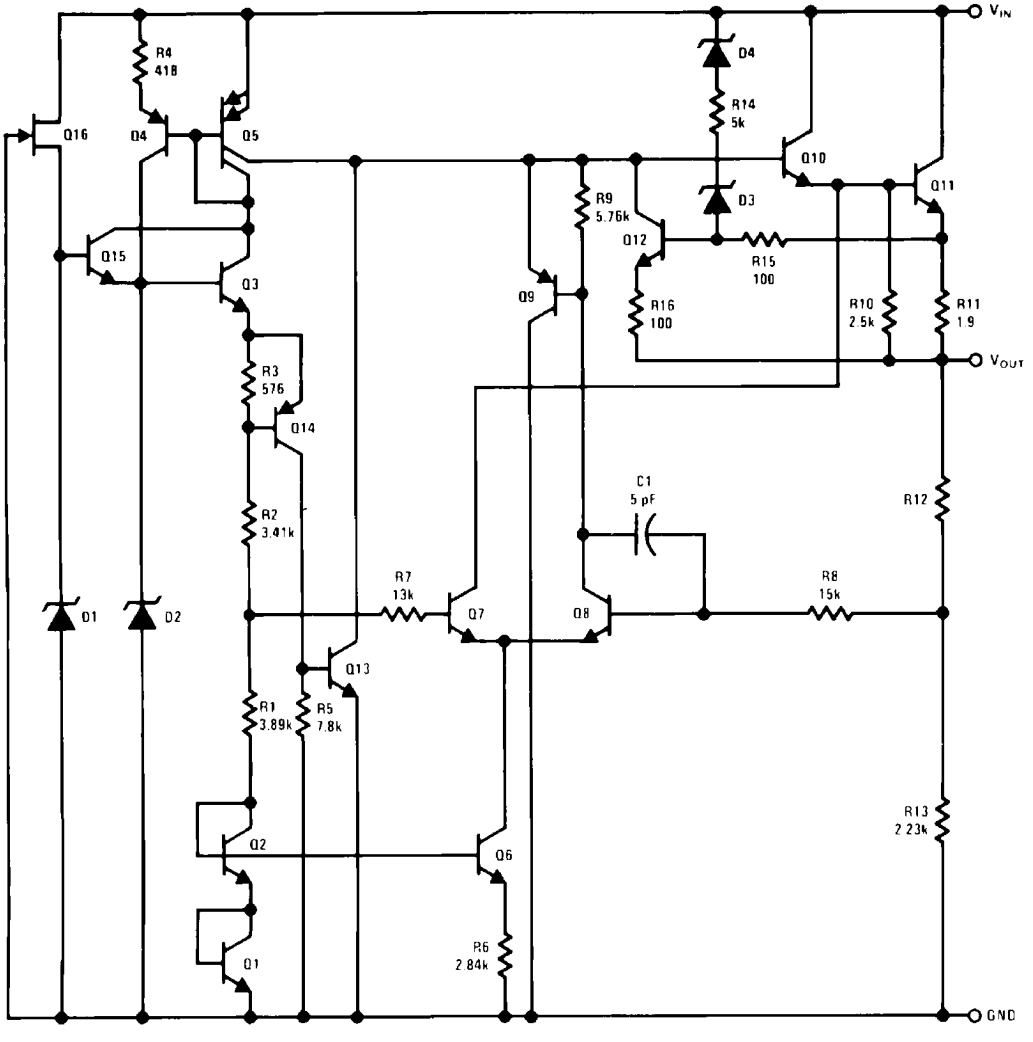
Quiescent Current



774421

Equivalent Circuit

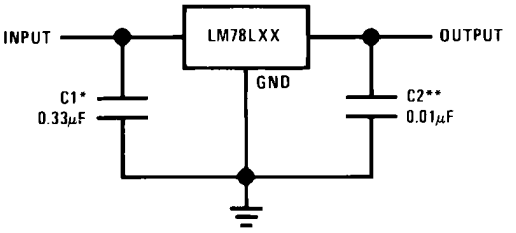
LM78LXX



774407

Typical Applications

Fixed Output Regulator

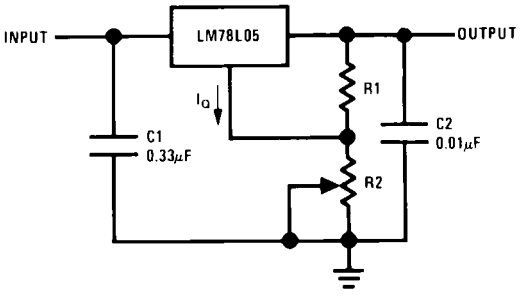


774408

*Required if the regulator is located more than 3″ from the power supply filter.

**See (*Note 4*) in the electrical characteristics table.

Adjustable Output Regulator

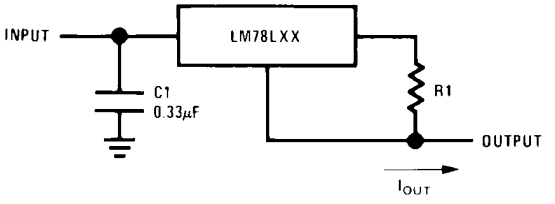


774409

V_{OUT} = 5V + (5V/R1 + I_Q) R2

5V/R1 > 3 I_Q, load regulation (L_r) ≈ [(R1 + R2)/R1] (L_r of LM78L05)

Current Regulator

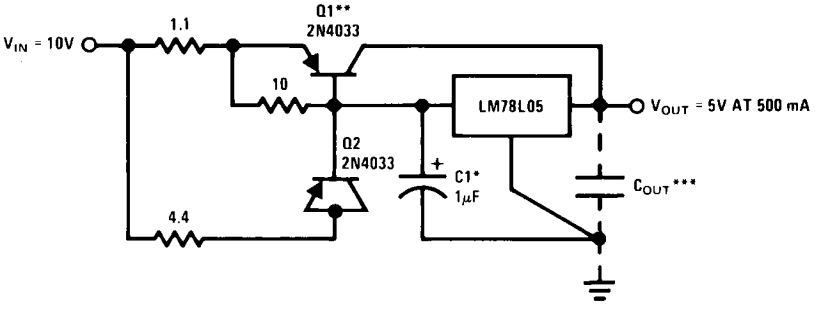


774410

I_{OUT} = (V_{OUT}/R1) + I_Q

>I_Q = 1.5mA over line and load changes

5V, 500mA Regulator with Short Circuit Protection



774411

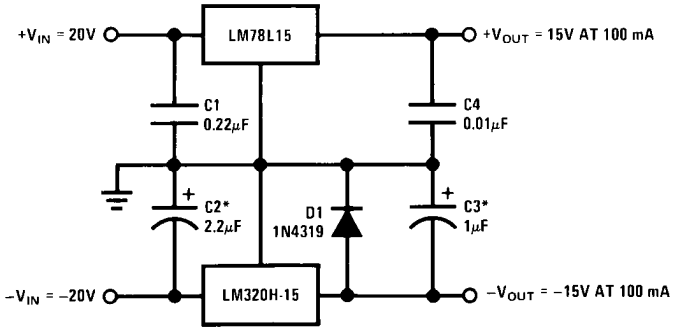
*Solid tantalum.

**Heat sink Q1.

***Optional: Improves ripple rejection and transient response.

Load Regulation: 0.6% 0 ≤ I_L ≤ 250mA pulsed with t_{ON} = 50ms.

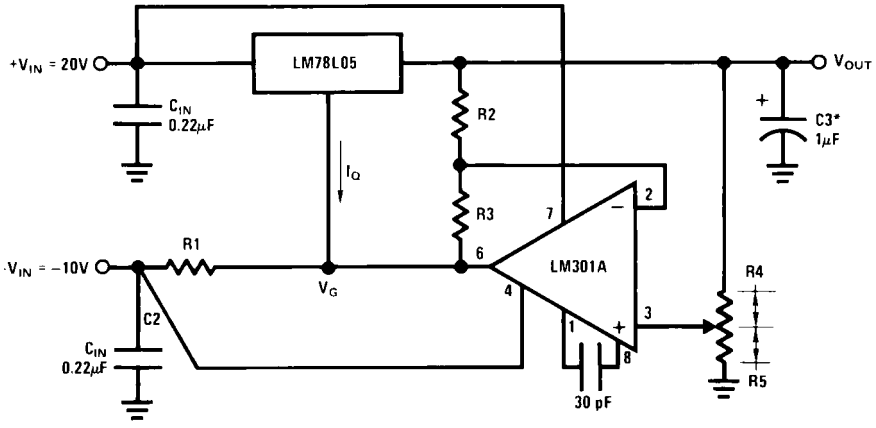
±15V, 100mA Dual Power Supply



774412

*Solid tantalum.

Variable Output Regulator 0.5V-18V



774413

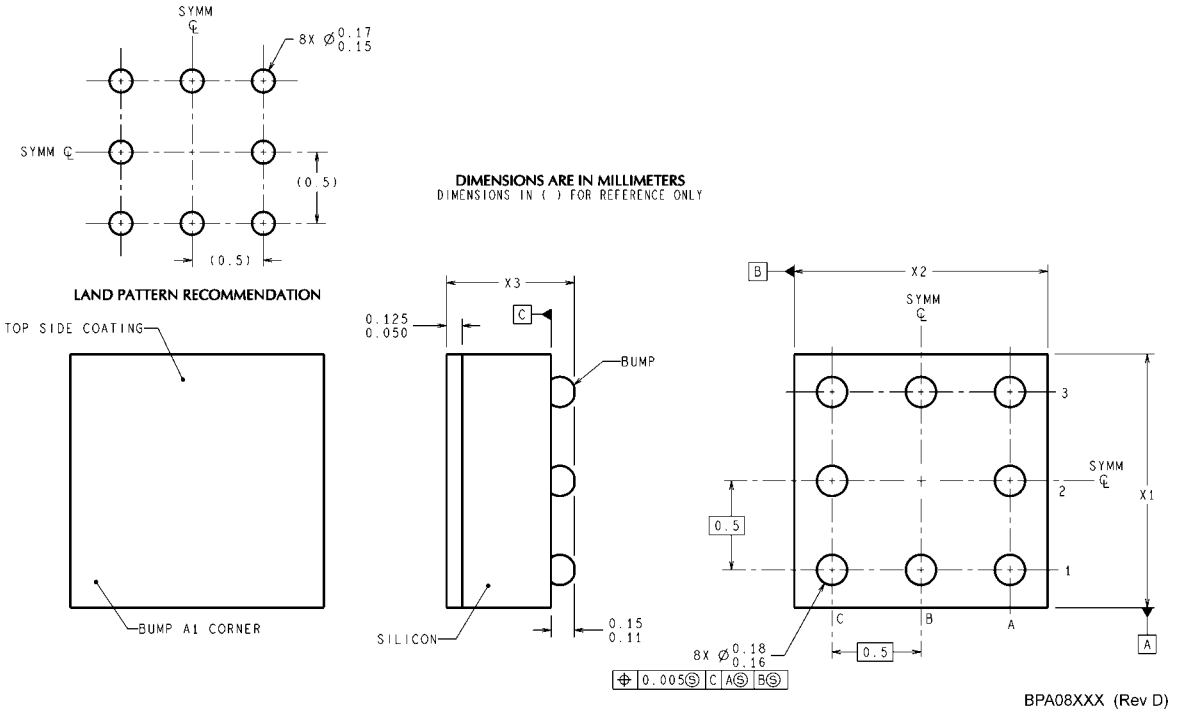
*Solid tantalum.

$V_{OUT} = V_G + 5V$, $R1 = (-V_{IN}/I_{Q\ LM78L05})$

$V_{OUT} = 5V\ (R2/R4)$ for $(R2 + R3) = (R4 + R5)$

A 0.5V output will correspond to $(R2/R4) = 0.1\ (R3/R4) = 0.9$

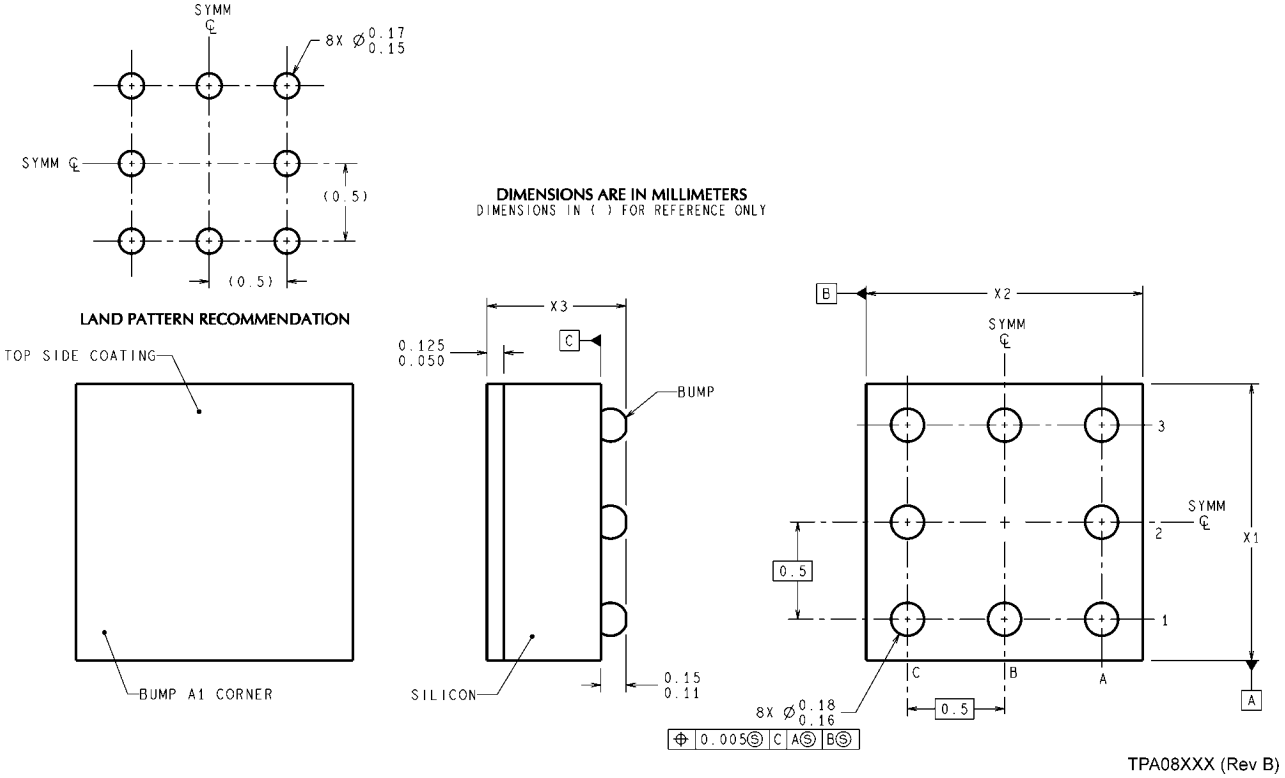
Physical Dimensions inches (millimeters) unless otherwise noted



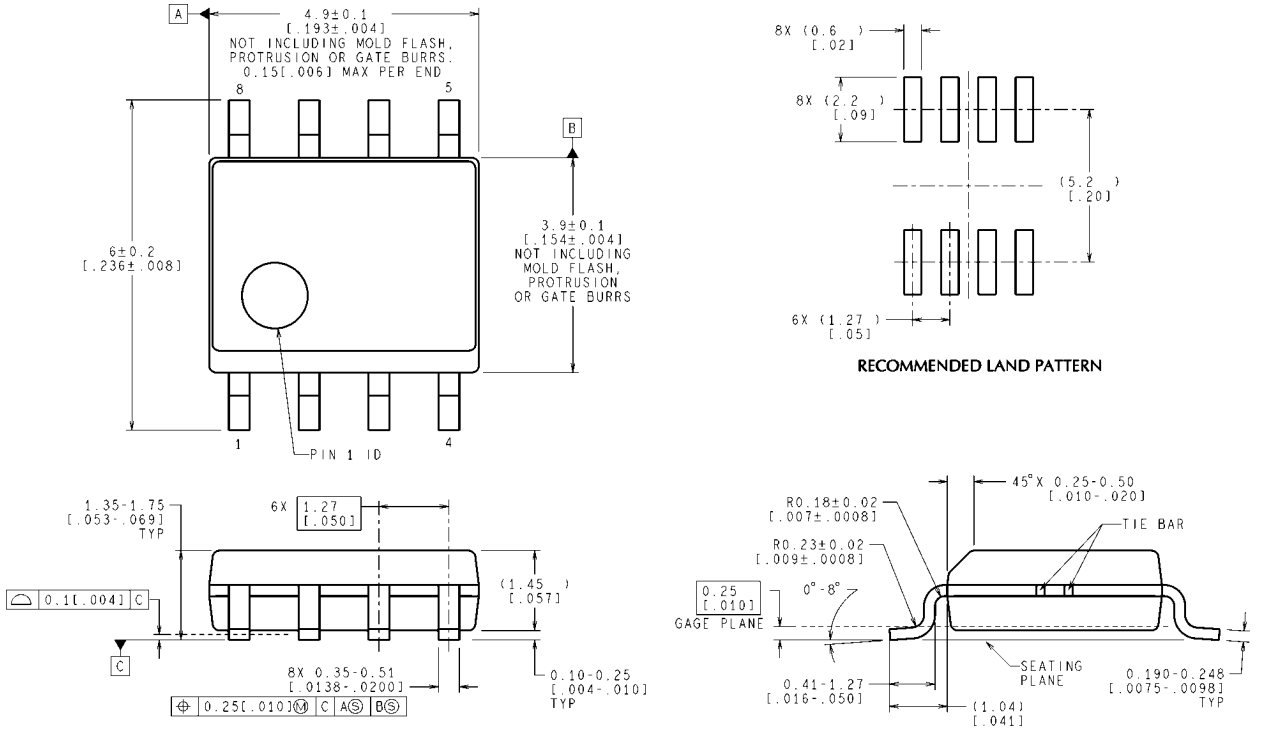
NOTES: UNLESS OTHERWISE SPECIFIED

1. EPOXY COATING
2. 63Sn/37Pb EUTECTIC BUMP
3. RECOMMEND NON-SOLDER MASK DEFINED LANDING PAD.
4. PIN A1 IS ESTABLISHED BY LOWER LEFT CORNER WITH RESPECT TO TEXT ORIENTATION. REMAINING PINS ARE NUMBERED COUNTERCLOCKWISE.
5. XXX IN DRAWING NUMBER REPRESENTS PACKAGE SIZE VARIATION WHERE X_1 IS PACKAGE WIDTH, X_2 IS PACKAGE LENGTH AND X_3 IS PACKAGE HEIGHT.
6. REFERENCE JEDEC REGISTRATION MO-211, VARIATION BC.

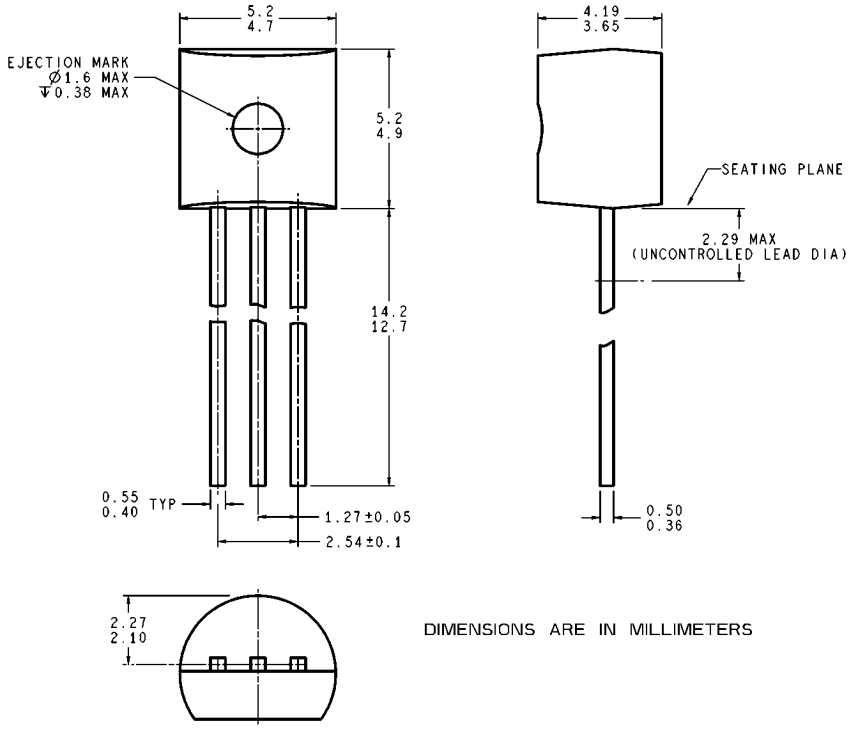
8-Bump micro SMD
NS Package Number BPA08AAB
X1 = 1.285mm X2 = 1.285mm X3 = 0.850mm



8-Bump Thin micro SMD
NS Package Number TPA08AAA
X1 = 1.285mm X2 = 1.285mm X3 = 0.500mm



S.O. Package (M)
NS Package Number M08A



Molded Offset TO-92 (Z)
NS Package Number Z03A

Notes

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		**Applications**	
Audio	www.ti.com/audio	Automotive and Transportation	www.ti.com/automotive
Amplifiers	amplifier.ti.com	Communications and Telecom	www.ti.com/communications
Data Converters	dataconverter.ti.com	Computers and Peripherals	www.ti.com/computers
DLP® Products	www.dlp.com	Consumer Electronics	www.ti.com/consumer-apps
DSP	dsp.ti.com	Energy and Lighting	www.ti.com/energy
Clocks and Timers	www.ti.com/clocks	Industrial	www.ti.com/industrial
Interface	interface.ti.com	Medical	www.ti.com/medical
Logic	logic.ti.com	Security	www.ti.com/security
Power Mgmt	power.ti.com	Space, Avionics and Defense	www.ti.com/space-avionics-defense
Microcontrollers	microcontroller.ti.com	Video and Imaging	www.ti.com/video
RFID	www.ti-rfid.com		
OMAP Mobile Processors	www.ti.com/omap		
Wireless Connectivity	www.ti.com/wirelessconnectivity		
	TI E2E Community Home Page		e2e.ti.com

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265