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NCE30P25S

NCE P-Channel Enhancement Mode Power MOSFET

Description

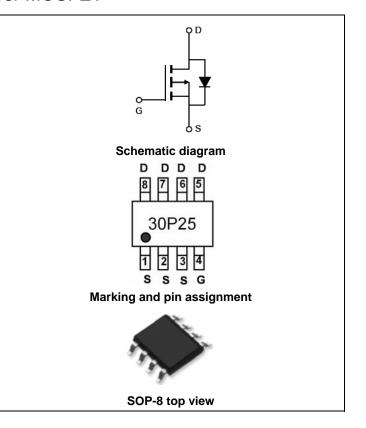
The NCE30P25S uses advanced trench technology to provide excellent $R_{DS(ON)}$, This device is suitable for use as a load switch or power management.

General Features

- $V_{DS} = -30V, I_{D} = -25A$ $R_{DS(ON)}$ <9m Ω @ V_{GS} =-10V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Power management
- Load switch



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
30P25	NCE30P25S	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-25	Α
Drain Current-Pulsed (Note 1)	I _{DM}	-70	Α
Maximum Power Dissipation	P _D	3.5	W
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	36	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μA



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Gate-Body Leakage Current	I_{GSS}	V_{GS} =±20 V , V_{DS} =0 V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	reshold Voltage $V_{GS(th)}$ V_{DS} = V_{GS} , I_D =-250 μ A		-1.0	-1.5	-2.5	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-15A	ı	6.4	9	mΩ	
Dialii-Source Oil-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-10A	-	8.3	14	11122	
Forward Transconductance	G FS	V _{DS} =-10V,I _D =-15A	30	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	\/ - 15\/\/ -0\/	-	3960	-	PF	
Output Capacitance	Coss	V_{DS} =-15V, V_{GS} =0V, F=1.0MHz	-	486	-	PF	
Reverse Transfer Capacitance	C _{rss}	Γ-1.UIVIΠ2	-	268	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	$t_{d(on)}$		-	20	-	nS	
Turn-on Rise Time	t _r	V _{DD} =-15V, ID=-10A,	-	13	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =3 Ω	-	55	-	nS	
Turn-Off Fall Time	t _f		-	21	-	nS	
Total Gate Charge	Q_g		-	65	-	nC	
Gate-Source Charge	Q_{gs}	V _{DS} =-15V,I _D =-10A,V _{GS} =-10V	-	12	-	nC	
Gate-Drain Charge	Q_{gd}		-	14	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =-25A	-	-	-1.2	V	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

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Typical Electrical and Thermal Characteristics

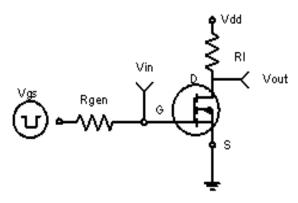
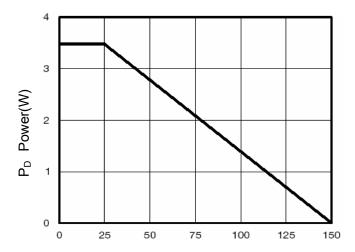


Figure 1 Switching Test Circuit



 T_J -Junction Temperature($^{\circ}$ C) **Figure 3 Power Dissipation**

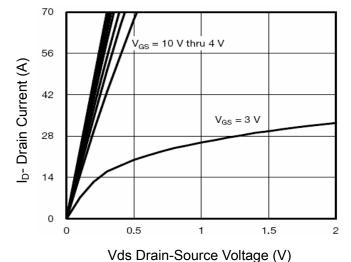


Figure 5 Output Characteristics

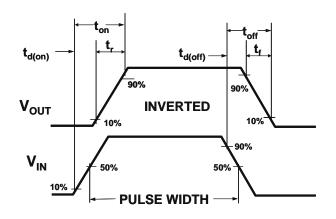


Figure 2 Switching Waveforms

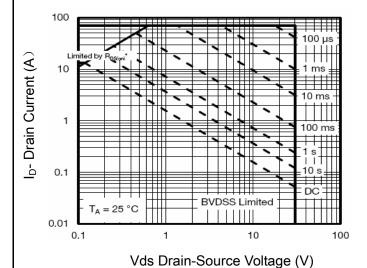


Figure 4 Safe Operation Area

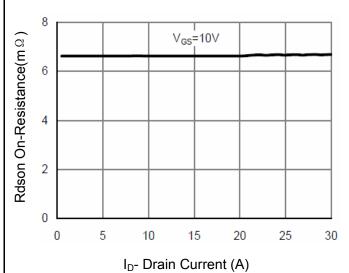
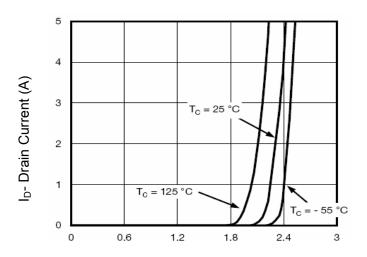
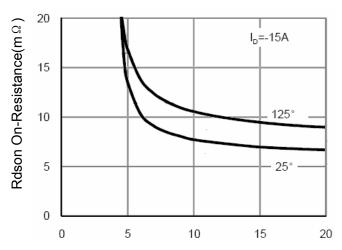


Figure 6 Drain-Source On-Resistance



Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

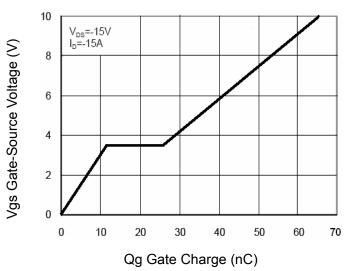
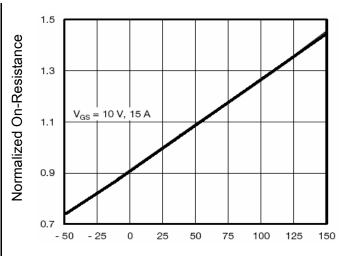


Figure 11 Gate Charge



T_J-Junction Temperature(°C)

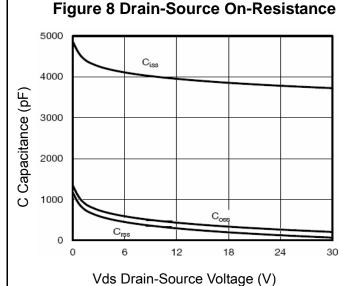


Figure 10 Capacitance vs Vds

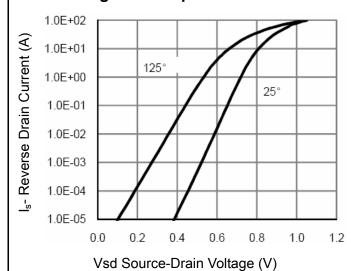


Figure 12 Source- Drain Diode Forward

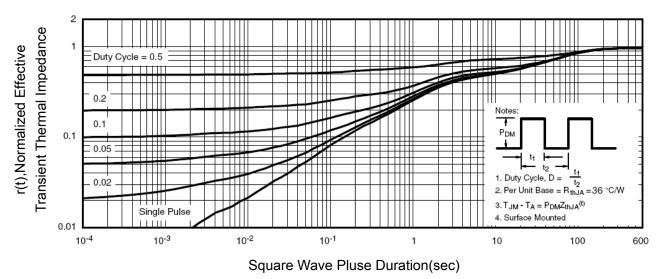


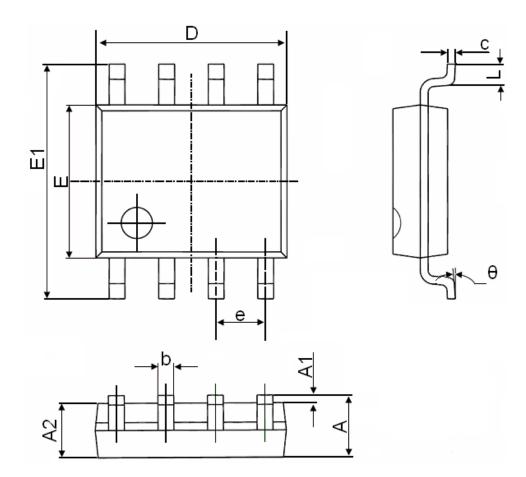
Figure 13 Normalized Maximum Transient Thermal Impedance

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SOP-8 Package Information



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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