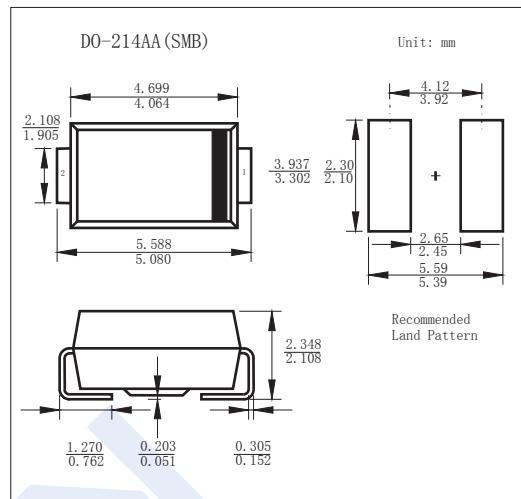


## Rectifier Diodes

### US2A ~ US2M

#### ■ Features

- Super Fast Switching For High Efficiency
- Low Forward Voltage Drop And High Current Capability
- Low Reverse Leakage Current
- Glass Passivated Chip



#### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	US2A	US2B	US2C	US2D	US2G	US2J	US2K	US2M	Unit				
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	50	100	150	200	400	600	800	1000	V				
RMS Voltage	V <sub>RMS</sub>	35	70	105	140	280	420	560	700					
Maximum DC Blocking Voltage	V <sub>DC</sub>	50	100	150	200	400	600	800	1000					
Forward Voltage@ T <sub>J</sub> =25°C I <sub>FM</sub> =2A	V <sub>F</sub>	1			1.4			1.7						
Averaged Forward Current@ T <sub>L</sub> =110°C	I <sub>FAV</sub>	2								A				
Peak Forward Surge Current @ 8.3ms	I <sub>FSM</sub>	50												
Maximum DC Reverse Current T <sub>J</sub> =25°C T <sub>J</sub> =125°C	I <sub>R</sub>	5								μA				
		350												
Maximum Reverse Current (Note.1)	t <sub>rr</sub>	50				100				ns				
Typical Junction Capacitance (Note.2)	C <sub>j</sub>	28								pF				
Thermal Resistance.Junction- to-Ambient	R <sub>thJA</sub>	20								°C/W				
Junction Temperature	T <sub>j</sub>	150								°C				
Storage Temperature	T <sub>stg</sub>	-55 to 150												

Note.1: I<sub>F</sub>=0.5A, I<sub>R</sub>=1.0A,I<sub>rr</sub>=0.25A

Note.2: Measured at 1.0MHz, V<sub>R</sub>=4.0V

#### ■ Marking

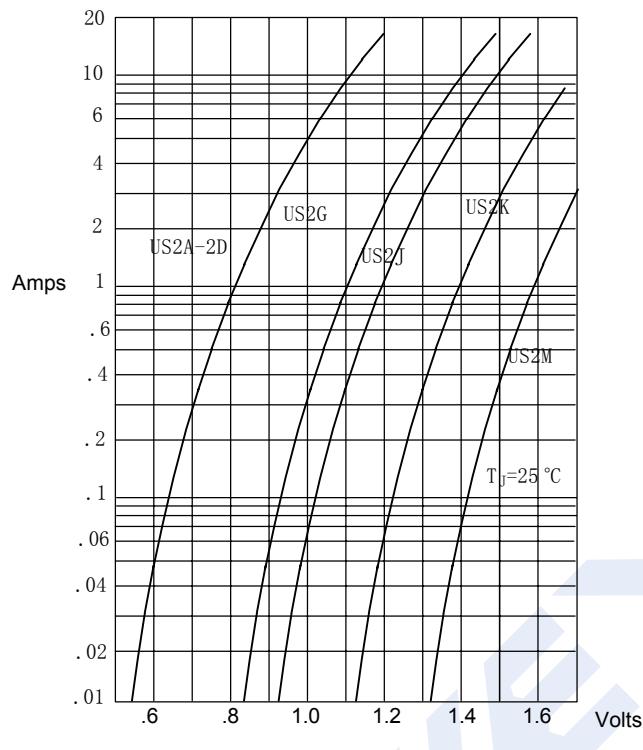
NO.	US2A	US2B	US2C	US2D	US2G	US2J	US2K	US2M
Marking	US2A	US2B	US2C	US2D	US2G	US2J	US2K	US2M

## Rectifier Diodes

### US2A ~ US2M

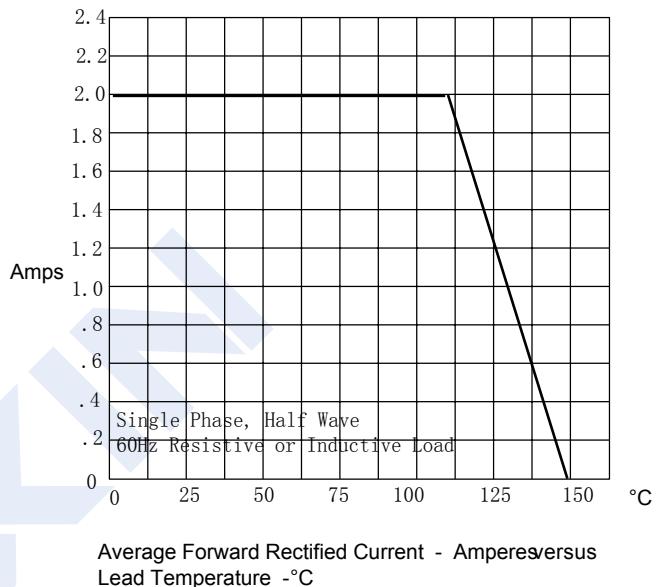
#### ■ Typical Characteristics

Figure 1  
Typical Forward Characteristics



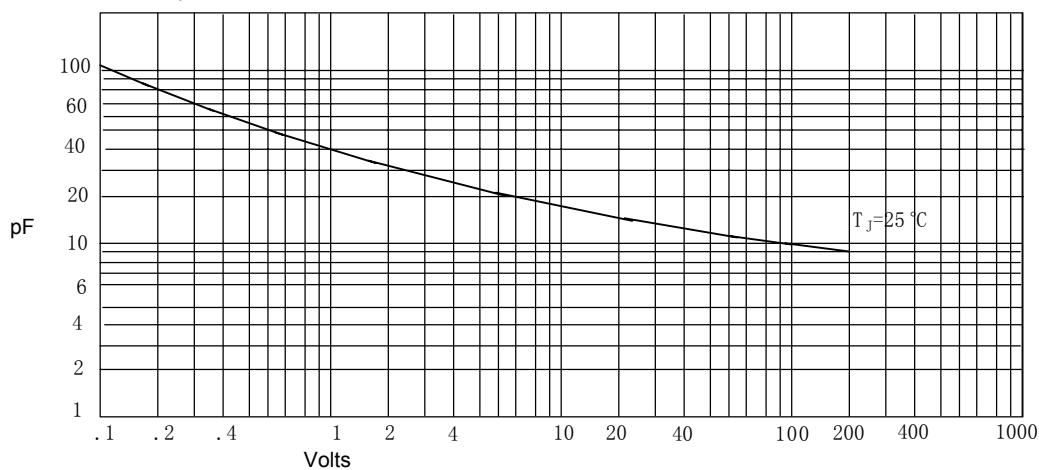
Instantaneous Forward Current - Amperesversus  
Instantaneous Forward Voltage - Volts

Figure 2  
Forward Derating Curve



Average Forward Rectified Current - Amperesversus  
Lead Temperature -  $^\circ\text{C}$

Figure 3  
Junction Capacitance



Junction Capacitance - pFversus  
Reverse Voltage - Volts

## Rectifier Diodes

### US2A ~ US2M

#### ■ Typical Characteristics

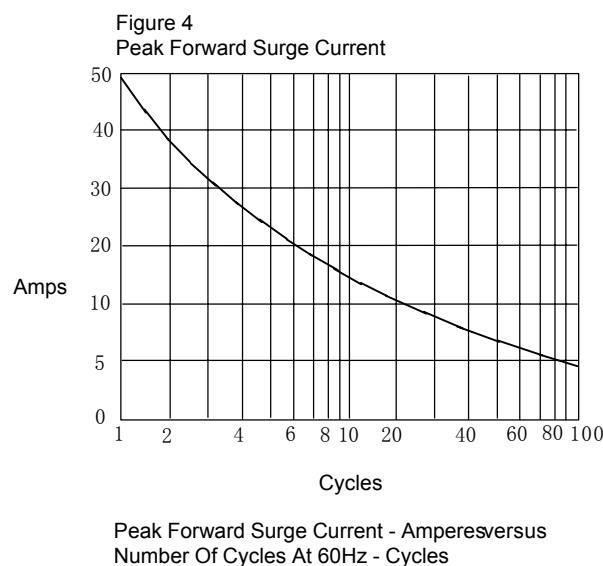


Figure 5  
Reverse Recovery Time Characteristic And Test Circuit Diagram

