

## 13NM60

Power MOSFET

13A, 600V N-CHANNEL  
SUPER-JUNCTION MOSFET

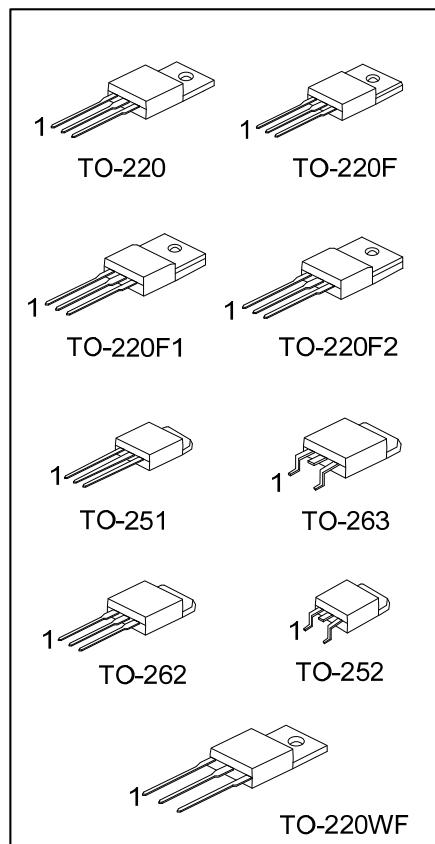
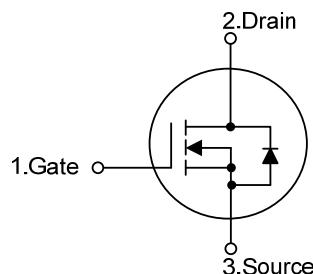
## ■ DESCRIPTION

The **UTC 13NM60** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

## ■ FEATURES

- \*  $R_{DS(ON)} \leq 0.45\Omega$  @  $V_{GS}=10V$ ,  $I_D=6.5A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

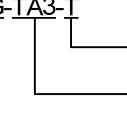
## ■ SYMBOL



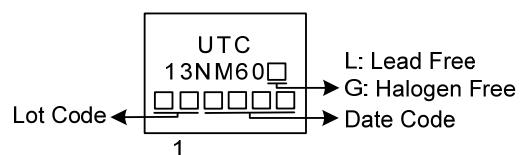
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
13NM60L-TA3-T	13NM60G-TA3-T	TO-220	G	D	S	Tube
13NM60L-TF1-T	13NM60G-TF1-T	TO-220F1	G	D	S	Tube
13NM60L-TF2-T	13NM60G-TF2-T	TO-220F2	G	D	S	Tube
13NM60L-TF3-T	13NM60G-TF3-T	TO-220F	G	D	S	Tube
13NM60L-TW1-T	13NM60G-TW1-T	TO-220WF	G	D	S	Tube
13NM60L-TM3-T	13NM60G-TM3-T	TO-251	G	D	S	Tube
13NM60L-TN3-R	13NM60G-TN3-R	TO-252	G	D	S	Tape Reel
13NM60L-T2Q-T	13NM60G-T2Q-T	TO-262	G	D	S	Tube
13NM60L-TQ2-T	13NM60G-TQ2-T	TO-263	G	D	S	Tube
13NM60L-TQ2-R	13NM60G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

 13NM60G-TA3-T	(1) Packing Type	(1) T: Tube, R: Tape Reel
	(2) Package Type	(2) TA3: TO-220, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F, TW1: TO-220WF, TM3: TO-251, TN3: TO-252, T2Q: TO-262, TQ2: TO-263
	(3) Green Package	(3) G: Halogen Free and Lead Free, L: Lead Free

## ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	13	A
	Pulsed (Note 2)	$I_{DM}$	39	A
Avalanche Current (Note 2)		$I_{AR}$	2.4	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	452	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220/TO-262/TO-263	$P_D$	120	W
	TO-220F/TO-220F1		30	W
	TO-220F2/TO-220WF		60	W
	TO-251/TO-252			
Junction Temperature	$T_J$		+150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$		-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L=157\text{mH}$ ,  $I_{AS}=2.4\text{A}$ ,  $V_{DD}=50\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$

4.  $I_{SD} \leq 13\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	$^\circ\text{C/W}$
	TO-220F1/TO-220F2			
Junction to Case	TO-220WF/TO-262	$\theta_{JC}$	110	$^\circ\text{C/W}$
	TO-263			
	TO-251/TO-252		1.04	$^\circ\text{C/W}$
	TO-220/TO-262/TO-263		4.2	$^\circ\text{C/W}$
Junction to Case	TO-220F/TO-220F1		2.08	$^\circ\text{C/W}$
	TO-220WF/TO-220F2			
	TO-251/TO-252 (Note)			

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

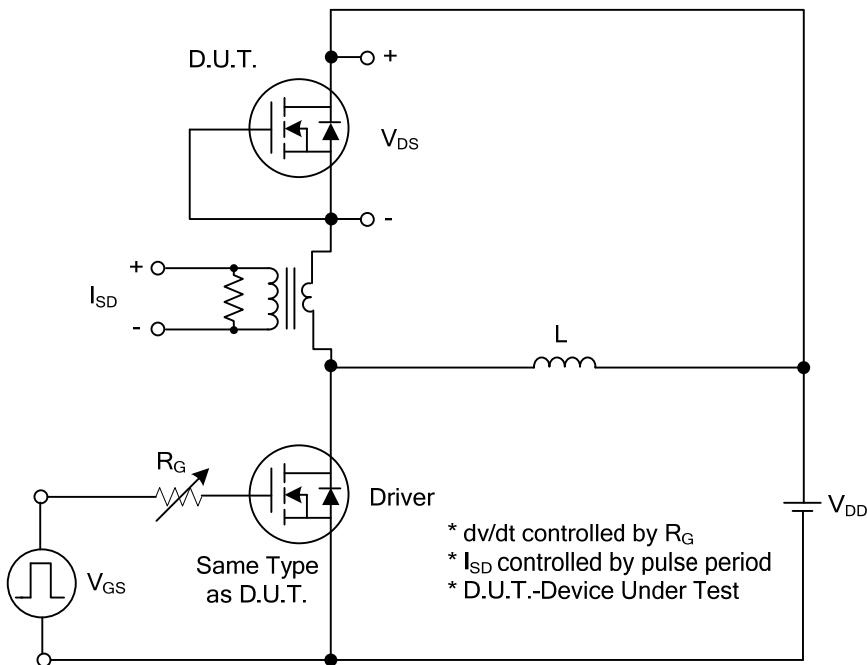
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 250\mu\text{A}$	600			V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}} = 600\text{V}, \text{V}_{\text{GS}} = 0\text{V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}} = 30\text{V}, \text{V}_{\text{DS}} = 0\text{V}$		100	nA	
	Reverse	$\text{V}_{\text{GS}} = -30\text{V}, \text{V}_{\text{DS}} = 0\text{V}$		-100	nA	
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}} = 10\text{V}, \text{I}_D = 6.5\text{A}$		0.45		$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		760		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			600		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			65		pF
Gate Resistance	$\text{R}_G$	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1\text{MHz}$		1.185		$\Omega$
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$\text{Q}_G$	$\text{V}_{\text{DS}}=480\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=13\text{A}, \text{I}_G=1\text{mA}$ (Note 1,2)		28.5		nC
Gate to Source Charge	$\text{Q}_{\text{GS}}$			4.5		nC
Gate to Drain Charge	$\text{Q}_{\text{GD}}$			10		nC
Turn-ON Delay Time (Note 1)	$t_{\text{D(ON)}}$	$\text{V}_{\text{DD}}=100\text{V}, \text{I}_D=13\text{A}, \text{R}_G=25\Omega, \text{V}_{\text{GS}}=10\text{V}$ (Note 1,2)		10		nS
Rise Time	$t_R$			24		nS
Turn-OFF Delay Time	$t_{\text{D(OFF)}}$			80		nS
Fall-Time	$t_F$			47		nS
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$\text{I}_S$				13	A
Maximum Body-Diode Pulsed Current	$\text{I}_{\text{SM}}$				39	A
Drain-Source Diode Forward Voltage (Note 1)	$\text{V}_{\text{SD}}$	$\text{I}_S=13\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	$t_{\text{rr}}$	$\text{I}_S=13\text{A}, \text{V}_{\text{GS}}=0\text{V}, \text{dI}_F/\text{dt}=100\text{A}/\mu\text{s}$		370		nS
Body Diode Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			5.4		$\mu\text{C}$

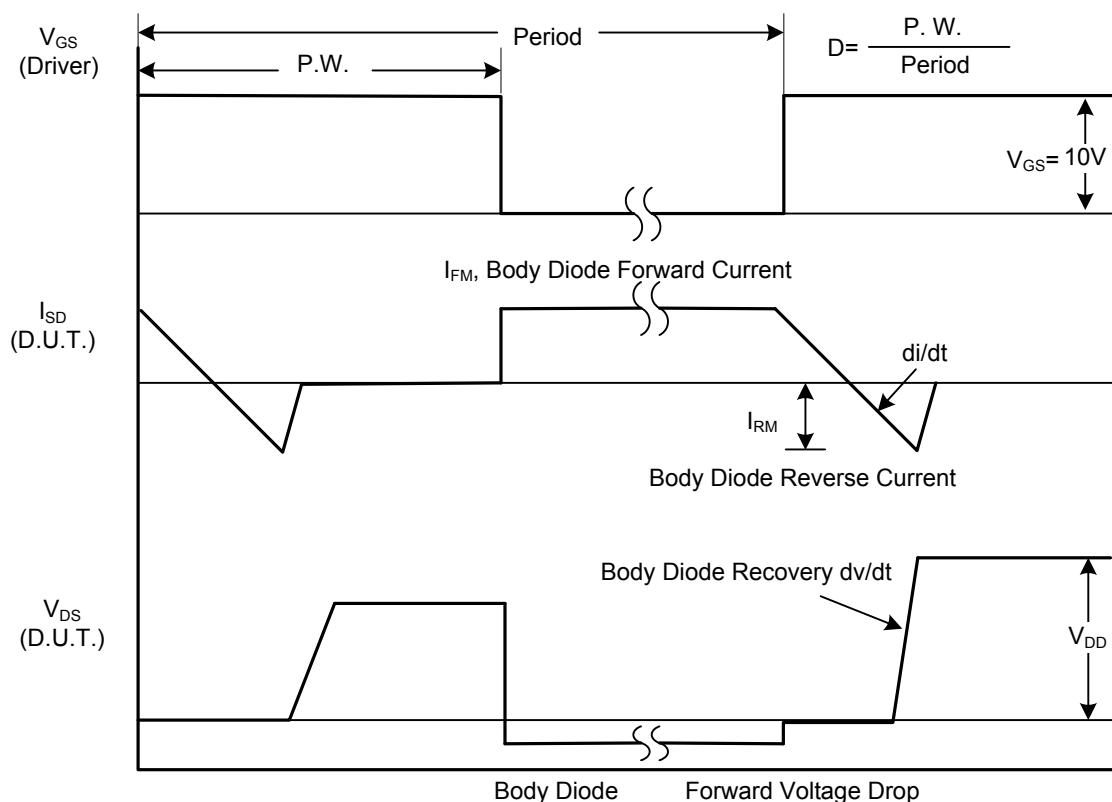
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

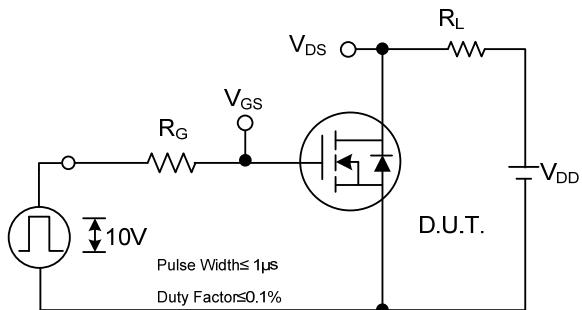


Peak Diode Recovery dv/dt Test Circuit

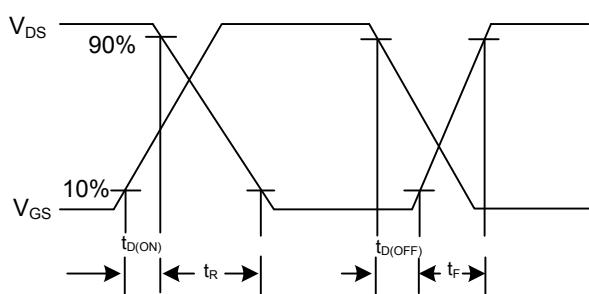


Peak Diode Recovery dv/dt Waveforms

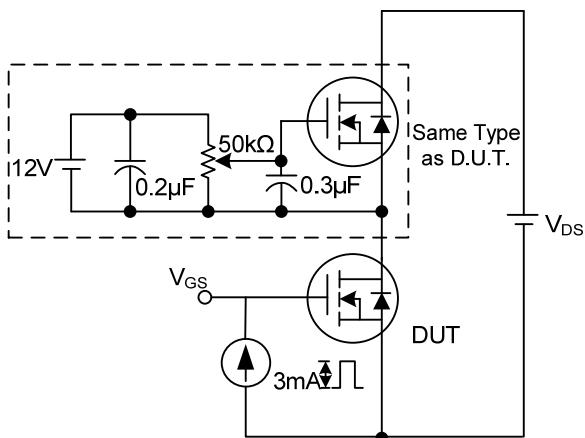
■ TEST CIRCUITS AND WAVEFORMS



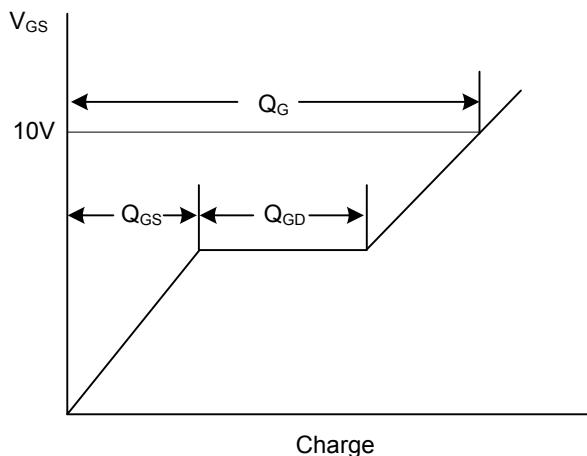
Switching Test Circuit



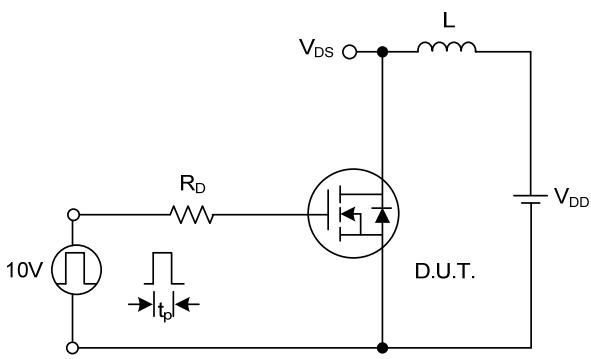
Switching Waveforms



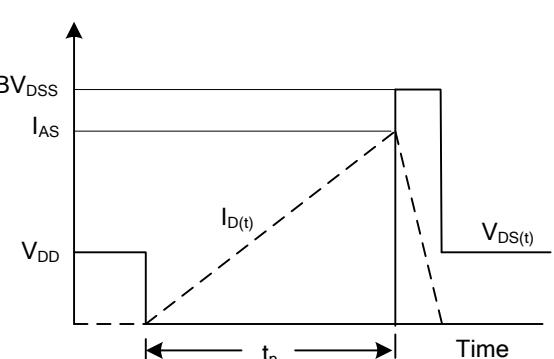
Gate Charge Test Circuit



Gate Charge Waveform

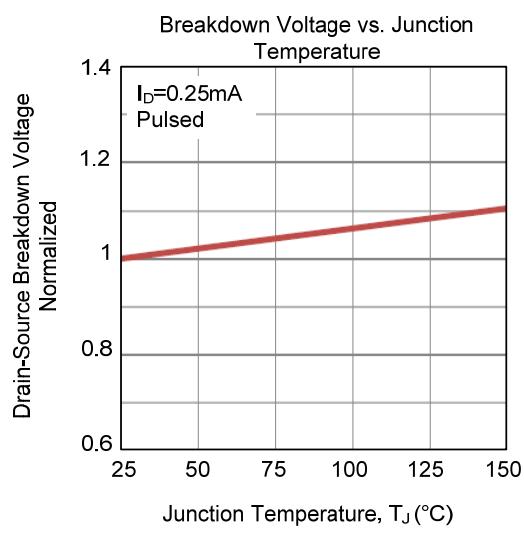
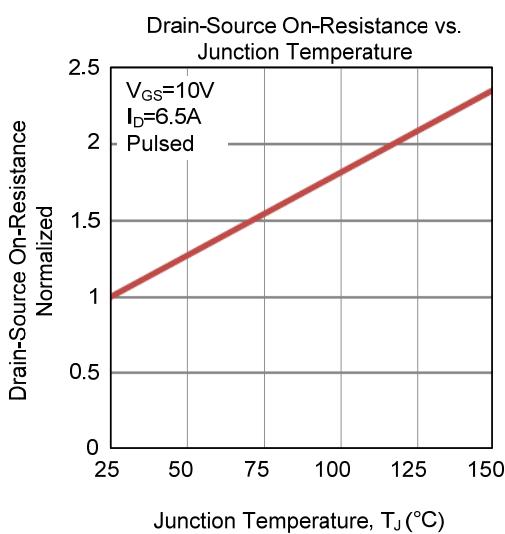
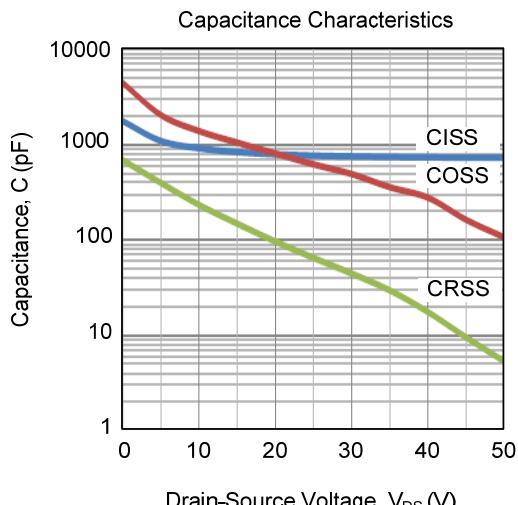
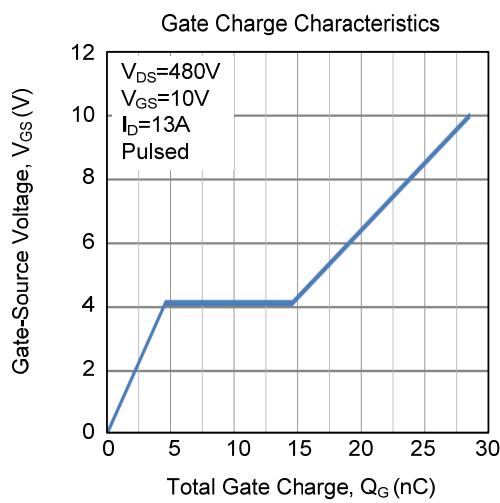
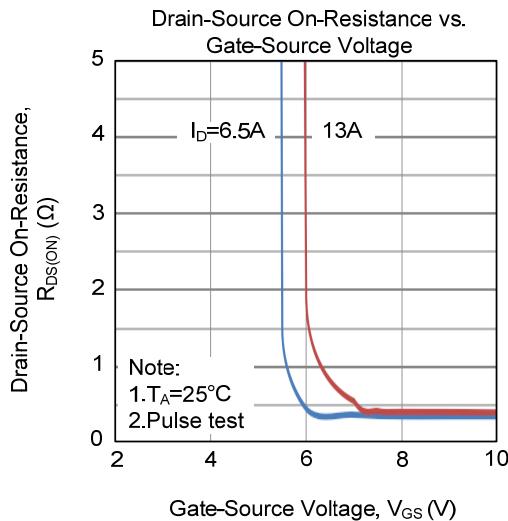
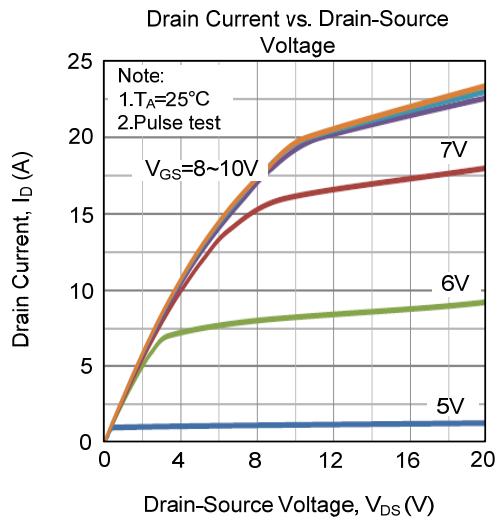


Unclamped Inductive Switching Test Circuit

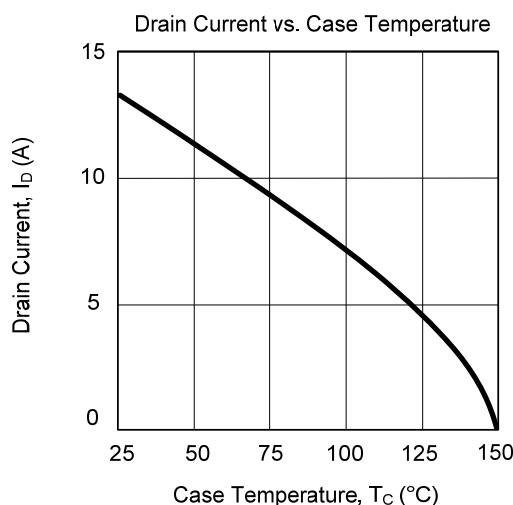
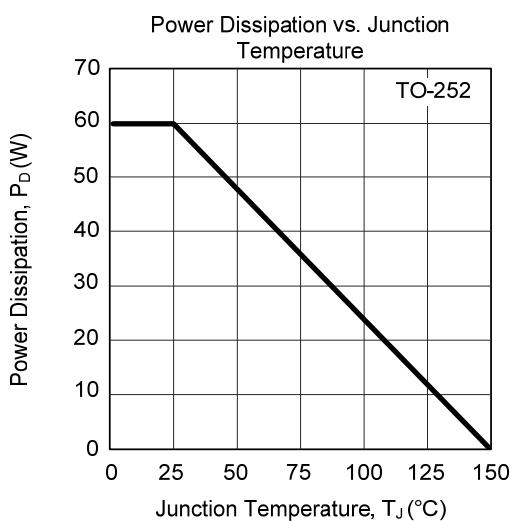
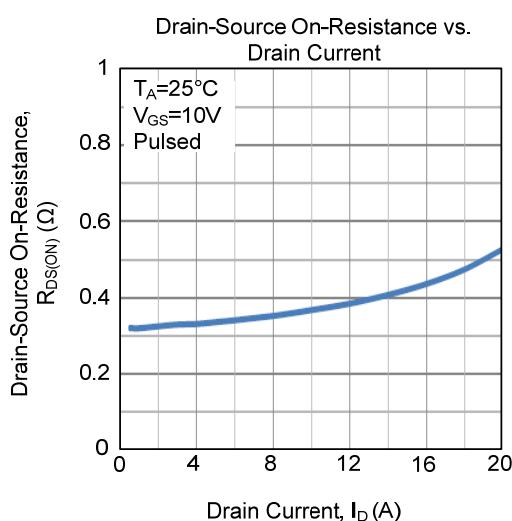
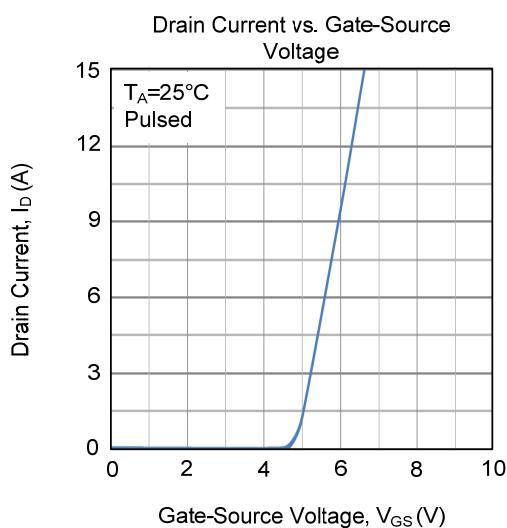
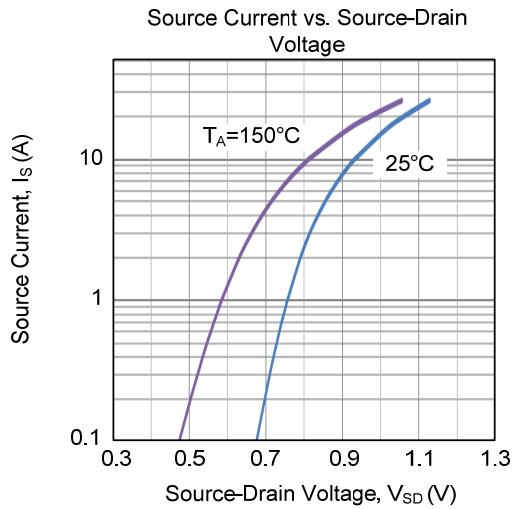
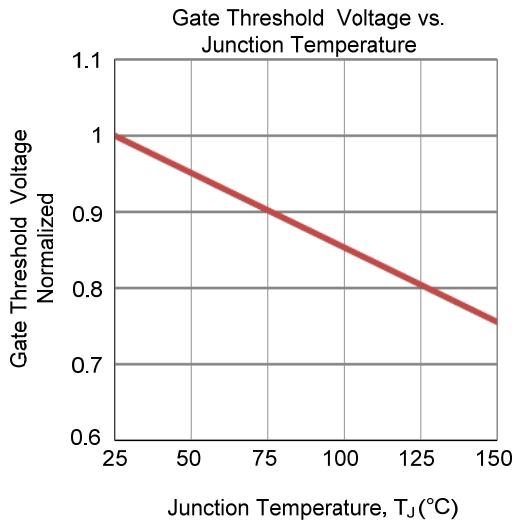


Unclamped Inductive Switching Waveforms

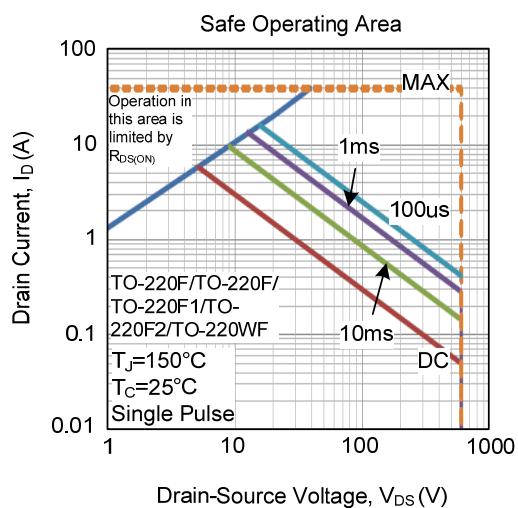
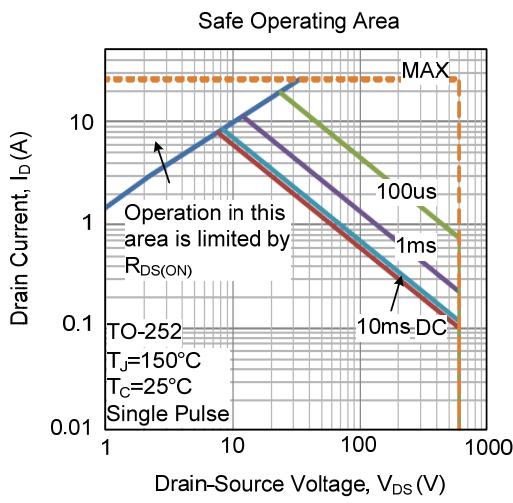
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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