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-500 V, -2.7 A, 4.9 Ω

January 2016

FQP3P50

P-Channel QFET® MOSFET

Description

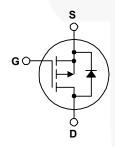
This P-Channel enhancement mode power MOSFET is $ext{-}2.7 \text{ A}$, -500 V, $R_{DS(on)}$ = 4.9 Ω (Max.) @ V_{GS} = -10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state

Low Gate Charge (Typ. 18 nC) resistance, and to provide superior switching performance • Low Crss (Typ 9.5 pF) and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, • 100% Avalanche Tested DC motor control, and variable switching power applications.

Features

- $I_D = -1.35 A$





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter		FQP3P50	Unit	
V _{DSS}	Drain-Source Voltage		-500	V	
I _D	Drain Current - Continuous (T _C = 25°C)		-2.7	Α	
	- Continuous (T _C = 100°C)		-1.71	Α	
I _{DM}	Drain Current - Pulsed	Note 1)	-10.8	А	
V_{GSS}	Gate-Source Voltage		± 30	V	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		250	mJ	
I _{AR}	Avalanche Current (Note 1)		-2.7	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		8.5	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		-4.5	V/ns	
P_{D}	Power Dissipation (T _C = 25°C)		85	W	
	- Derate above 25°C		0.68	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C	
T _L	Maximum lead temperature for soldering, 1/8" from case for 5 seconds		300	°C	

Thermal Characteristics

Symbol	Parameter	FQP3P50	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.47	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQP3P50	FQP3P50	TO-220	Tube	N/A	N/A	50 units

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Uni
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	-500			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = -250 μ A, Referenced to 25°C		0.42		V/°(
I _{DSS}	Zara Cata Valta da Dunia Comunat	V _{DS} = -500 V, V _{GS} = 0 V			-1	μΑ
	Zero Gate Voltage Drain Current	V _{DS} = -400 V, T _C = 125°C			-10	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 30 V, V _{DS} = 0 V			100	nΑ
On Cha	racteristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$	-3.0		-5.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = -10 V, I _D = -1.35 A		3.9	4.9	Ω
9 _{FS}	Forward Transconductance	V _{DS} = -50 V, I _D = -1.35 A		2.35		S
Dynami	ic Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		510	660	pF
C _{oss}	Output Capacitance			70	90	pF
C _{rss}	Reverse Transfer Capacitance			9.5	12	pF
	ng Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = -250 V, I _D = -2.7 A,		12	35	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		56	120	ns
t _{d(off)}	Turn-Off Delay Time			35	80	ns
t _f	Turn-Off Fall Time	(Note 4)	-	45	100	ns
Qg	Total Gate Charge	$V_{DS} = -400 \text{ V}, I_{D} = -2.7 \text{ A},$		18	23	nC
Q _{gs}	Gate-Source Charge	V _{GS} = -10 V		3.6		nC
Q _{gd}	Gate-Drain Charge	(Note 4)	/	9.2		nC

Drain-Source Diode Characteristics and Maximum Ratings

Is	Maximum Continuous Drain-Source Diode Forward Current				-2.7	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				-10.8	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = -2.7 \text{ A}$			-5.0	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V, } I_{S} = -2.7 \text{ A,}$		270		ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs		1.5		μC

Notes:

- 1. Repetitive rating : pulse-width limited by maximum junction temperature.
- 2. L = 62 mH, I_{AS} = -2.7 A, V_{DD} = -50 V, R_G = 25 Ω , starting T_J = 25°C. 3. I_{SD} ≤ -2.7 A, di/dt ≤ 200 A/µs , V_{DD} ≤ BV_{DSS}, starting T_J = 25°C. 4. Essentially independent of operating temperature.

Typical Characteristics

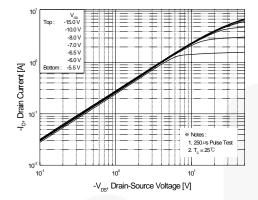


Figure 1. On-Region Characteristics

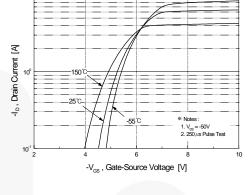


Figure 2. Transfer Characteristics

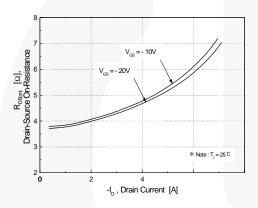


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

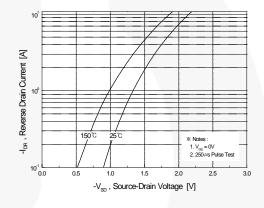


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

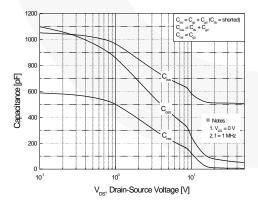


Figure 5. Capacitance Characteristics

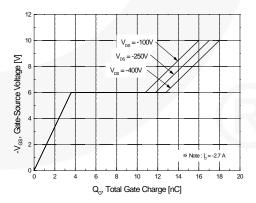


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

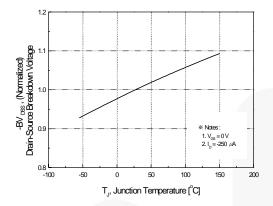


Figure 7. Breakdown Voltage Variation vs. Temperature

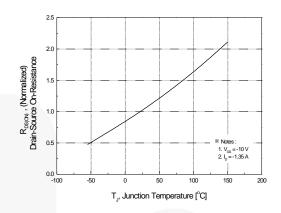


Figure 8. On-Resistance Variation vs. Temperature

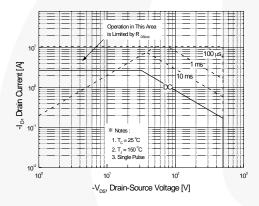


Figure 9. Maximum Safe Operating Area

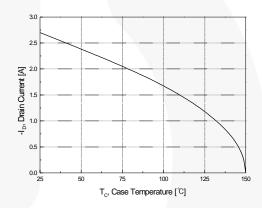


Figure 10. Maximum Drain Current vs. Case Temperature

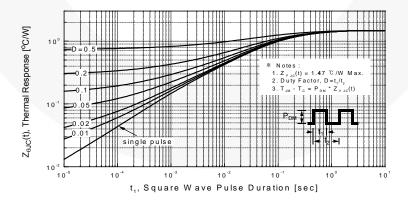


Figure 11. Transient Thermal Response Curve

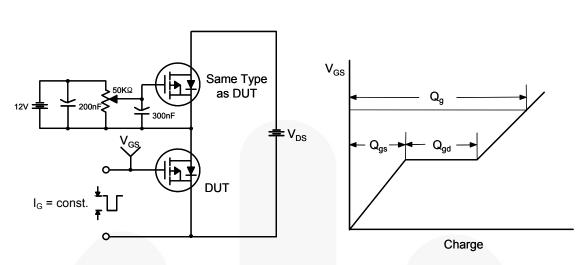


Figure 12. Gate Charge Test Circuit & Waveform

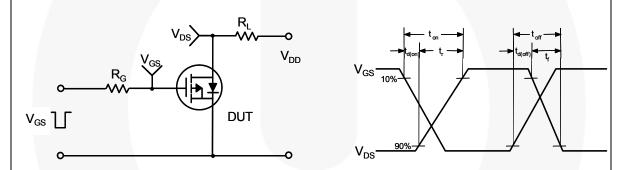


Figure 13. Resistive Switching Test Circuit & Waveforms

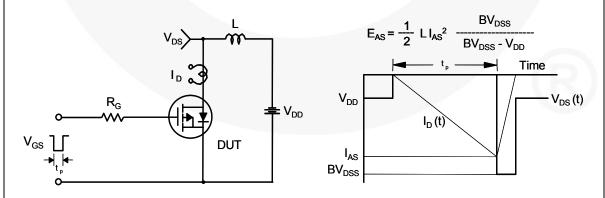
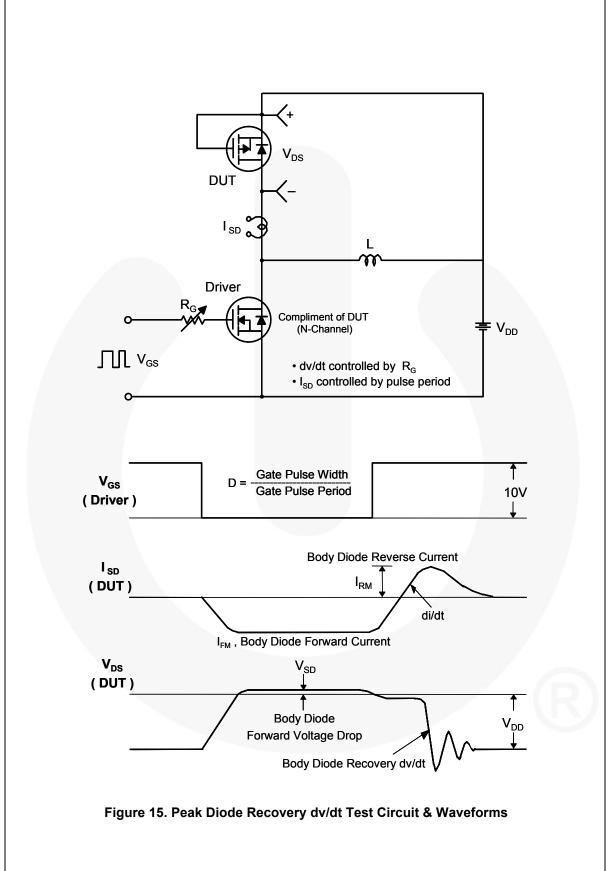


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



Mechanical Dimensions

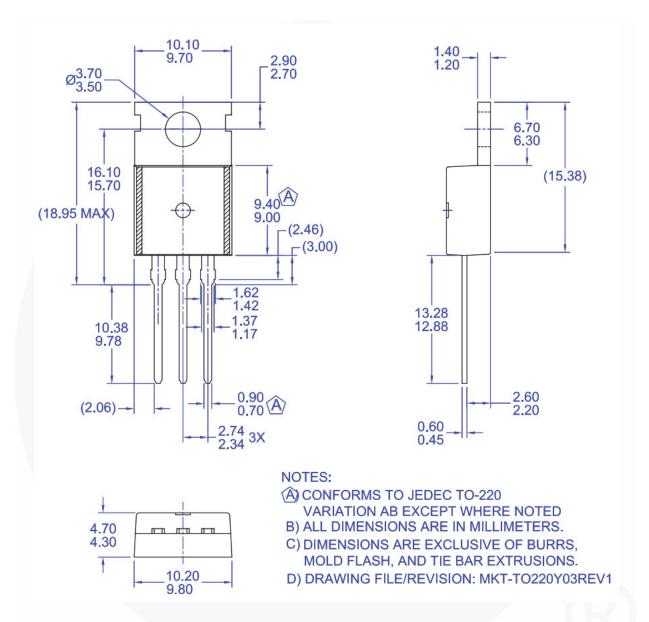


Figure 16. TO220, Molded, 3-Lead, Jedec Variation AB

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